

REMARKS

Claims 1, 12, and 39 have been amended to incorporate the subject matter from claim 11 (or claim 21) and to point out the common-source differential-pair function that these devices play in the circuit. It is respectfully urged that claim 1 distinguishes over the prior art.


With respect to claims 2, 4, and 6, the Examiner states that "the Buhler reference discloses including means for selectively pulsing (VDD signal) said source follower drain supply voltage node (See Col. 3, lines 20-30)."

This rejection is respectfully traversed. The Buhler reference discloses no such thing. The passage in the Buhler specification cited by the Examiner merely states that there is a supply voltage node. The Buhler reference does not disclose or suggest that the supply voltage node be pulsed as required by claim 2. Supply voltage nodes that carry VDD potentials are not generally pulsed and there is nothing in Buhler to suggest that they should be pulsed. The mere disclosure of a power supply voltage node is not a disclosure that such a node should be pulsed as required by claim 2.

It is believed that these amendments distinguish over the prior art and that the claims are allowable in their present form.

If the Examiner has any questions regarding this application, the Examiner may telephone the undersigned at 775-586-9500.

Respectfully submitted,
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